

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09/832,685</u>	Prepared by <u>Lois Stone</u>	Tracking Number <u>5926800</u>	
Examiner-GAU <u>Chang - 2819</u>	Date <u>7/20/04</u>	Week Date <u>3/29/04</u>	
	No. of queries <u>1</u>	ZFW	

## JACKET

a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<u>p. PTO-1449</u>
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

## SPECIFICATION

- a. Page Missing
- b. Text Continuity
- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
- k. Other

## CLAIMS

- a. Claim(s) Missing
- b. Improper Dependency
- c. Duplicate Numbers
- d. Incorrect Numbering
- e. Index Disagrees
- f. Punctuation
- g. Amendments
- h. Bracketing
- i. Missing Text
- j. Duplicate Text
- k. Other

## MESSAGE

Please provide copies of the PTO-1449 forms with the citations either initialed or lined through. Copies provided for reference.

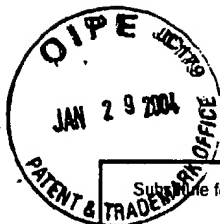
Thank you,

initials LS

## RESPONSE

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PTO/SB/08A (08-03)



PTO/SB/08A (08-03)

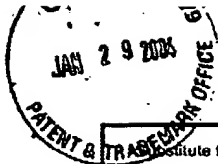
Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/832,685
		Filing Date	April 10, 2001
		First Named Inventor	Reddy, Srinivas
		Art Unit	2819
		Examiner Name	Daniel D. Chang
Sheet <b>2</b>	of	Attorney Docket Number	015114-045720US

		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>6</sup> (if known)			
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		Examiner Name	Daniel D. Chang		
Sheet	3	of		Attorney Docket Number	015114-045720US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	0033	Scarpino, Jr., Field-Effect Transistor Bidirectional Driver Control Circuit, IBM TDB, vol. 18, No. 8, pp. 2442-2443, Jan. 1974.	
	0034	Rhyne, Fundamentals of Digital Systems Design, N. J., pp 70-71, 1973.	
	0035	Anonymous, Tri-State Double inverter Buffer, Research Disclosure, p. 30511, Sep. 1989.	
	0038	Xilinx Corp., The Programmable Logic Data Book, "XC4000, XC4000A, XC4000H, Logic Cell Array Families," pp. 2-7 to 2-46 (1994).	
	0037	Xilinx Corp., The Programmable Logic Data Book, "XC3000, XC3000A, XC3000L, XC3100, XC3100A, Logic Cell Array Families," pp. 2-105 to 2-124 (1994).	
	0038	Xilinx Corp., The Programmable Logic Data Book, "XC3000, XC3000A, XC3000L, XC3100, XC3100A, Logic Cell Array Families," pp. 2-105 to 2-152 (1994).	
	0039	Altera Data Book, Altera Classic, Programmable Logic Device Family, pp. 235-244, 08/93.	
	0040	Altera Data Book, Max 5000 Programmable Logic Device Family, pp. 149-160, 08/93.	
	0041	Altera Data Book, Max 7000 Programmable Logic Cell Device Family, pp. 69-81, 08/93.	
	0042	Altera Data Book, Flex 8000 Programmable Logic Device Family, pp. 29-47, 08/93.	
	0043	Altera Data Book, Introduction, pp. 3-16, 08/93.	

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